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Page 1 of 4

Urgent

Confidential

Date: 29-February-08

To: Fax:

Examiner: Michael Pyzocha USPTO

(571) 273-8300

Art Unit: 2137

From: Thomas R. Lane Intel Corporation Fax: (703) 633-0915

M/S: CY-LF2

Subject: Application No.: 10/031,961

Filed: December 31, 2001 Inventors: Glew Docket No.: 42390P13736

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Thomas R. Lane Date: 2229/08

Included in this transmission: Fax Cover Sheet (1 page) Response to Office Action (3 pages)

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PAGE 1/4 * RCVD AT 2/29/2008 10:15:01 PM [Eastern Standard Time] * SVR:USPTO-EFXRF-6/27 * DNIS:2738300 * CSID: * DURATION (mm-ss):02-16

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42390P13736

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Glew, et al

for Intel Corporation

Serial No.: 10/039,961

Filing Date: December 31, 2001

For: PROCESSOR SUPPORTING EXECUTION OF AN

AUTHENTICATED CODE INSTRUCTION

Examiner: Pyzocha, Michael J.

Art Unit: 2137

CERTIFICATE OF MAILING/TRANSMISSION

I hereby certify that this correspondence is being facsimile transmitted to the United States Patent and Trademark Office on the date indicated below and that this paper has been addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, fax number (571) 273-8300.

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Commissioner for Patents PO Box 1450 Alexandria, VA 22313-1450

RESPONSE TO OFFICE ACTION

Dear Sir:

In response to the final office action mailed on November 30, 2007, consideration of the following remarks is respectfully requested.